	Notice of References Cited	Application/Control No. 10/602,116	Applicant(s)/Patent Under Reexamination ULRICH, HANNO	
		Examiner Ayal I. Sharon	Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Menascé, D.A. et al. Capacity Planning and Performance Modeling. © 1994. pp.iii-ix, 113-129, 153-159, and 263-266.
	V	Färber, G. et al. "Improving Processor Utilization with a Task Classification Model Based Application Specific Hard Real-Time Architecture." Proc. 4th Int'l Workshop on Real-Time Computing Systems and Applications. Oct. 27-29, 1997. pp.276-283.
	W	Lauzac, S. et al. "An Efficient RMS Admission Control and its Application to Multiprocessor Scheduling." Proc. of the 1st Merged Int'l ... and Symposium on Parallel and Distributed Processing. Mar.30-Apr.3, 1998. pp.511-518.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.